EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	0	ioput and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:45
L4	8	(bidirectional adj node) and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:46
L5	41	(analog and digital and bidirectional and ((mixed near signal) or mixed-signal) and simulation) and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:48
L6	269	703/14.ccls. and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:50

EAST Search History (Interference)

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L8	1814	(node and digital and analog).clm.	US-PGPUB	OR	ON	2009/09/15 17:18
L9	368	(node and digital and analog and state).clm.	US-PGPUB	OR	ON	2009/09/15 17:18
L10	9	(node and digital and analog and state and (high adj impedance)).clm.	US-PGPUB	OR	ON	2009/09/15 17:18

Google scholar | ioput | 1980   - | 2002 | Searc

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Did you mean: *input*

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F Hinedi, M Cases, S Dutta, RH Dennard - US Patent 5,986,472, 1999 - Google Patents
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transistor 118 is connected to an information node **IOPUT**, which is connected to ...
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A Hayatdavoudi, NC Durugbor, A Ghalambor, ... - SPE/IADC Drilling Conference, 1987 - onepetro.org
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Stacked PFET off-chip driver with a latch bias generator for overvoltage protection
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7.1 0irect Computation 7.2 Fast Fourisr Transform Algorithms 7.3 Recant 0evalopmants in ...
FF TRANSF0RM - Modern Signal Processing: Proceedings of the Arab ..., 1986 - books.google.com
... 1.2 Systeme If there in a usiqus outpet for acy givan inpot, the **ioput**-outpet
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F Hinedi, M Cases, S Dutta, RH Dennard - US Patent 5,867,010, 1999 - Google Patents
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G Hamlin - 1982 - osti.gov
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syntactic-level feedback for **ioput** fr- different devices. Our ...
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DONALD: a workbench for interactive design space exploration and sizing of **analog** circuits
K Swings, W Sansen - Design Automation. EDAC., Proceedings of the ..., 1991 - ieeexplore.ieee.org
... circuit sizing. The program is de to numerically invert an analytic design
model of **analog** circuit in any arbitrary direction. It ...
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... in digitel signal processing similar to exponantial fuectinns in **analog** signal
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PA Littlehales, CP Lewis, SR Bishop - Control, 1994. Control'94. International Conference on, 1994 -
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has then to be converted to a digital fomat for piocessing. ...
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J Schemmel, F Shurmann, S Hohmann, K ... - Neural Networks, 2002. IJCNN'02. Proceedings of the ..., 2002 -
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... art approach in between these two opposite poles by combining fully **analog** network
blocks ... the output at time t depends not oniy on the actual **ioput**, but also ...
Related articles

Method of improving fault current measurement accuracy on electronic reclosure control
PP Schumacher, JC Cummins - US Patent 5,701,080, 1997 - Google Patents
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the ... rc/ ?+(i)2+(i ??" selector inputs to fee eight-bit **analog** multiplexers (26 ...
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at the behavioral level is pro- posed. ... A. Describing an **Analog** Block ...
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technology, but will be impossible to install at 30 MHz. ...
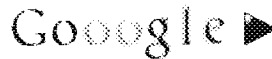Cited by 6 - Related articles - BL Direct

A systematic approach to the control of electrohydraulicservosystems
A Alleyne - American Control Conference, 1998. Proceedings of ..., 1998 - ieeexplore.ieee.org
... zero. Hence the output tracking error, 0u;uu'i**Oput** en = -k,e, (2.22)
en-r+l = y - Ydesired, converges to zero as t -+ w . 0 The ...
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[CITATION] 256-channel bidirectional optical interconnect using VCSELs and photodiodes on CMOS
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mapping in ... BPF, bandpass filter; A/D's, **analog–digital** converters. ...
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S Espejo, R Domínguez-Castro, G Liñán, Á ... - 1998 IEEE International Conference on Electronics, ..., 1998 -
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both with a width of 16-lines), and a con- trol bus comprising several ...
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High speed bidirectional digital cable transmission system
TM Paff, H Graham - US Patent 5,553,064, 1996 - Google Patents
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to **analog** form for ... 2. The high speed **bidirectional** cable transmission system ...
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Superconducting analog-to-digital converter with grounded four-junction squid bidirectional
...

GS Lee - US Patent 5,019,818, 1991 - Google Patents
... a converters and, more particularly, to high-speed, high- double-junction SQUID
quantizer and a **bidirectional** resolution **analog**-to-**digital** converters employing ...
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... BS cell, the output BS cell, and the **bidirectional** BS cell. Then the paper explains
the application of Boundary Scan to the testing of **analog-digital** ASKS in a ...
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.... : An optimal architecture and learning technique for **analog** VLSI feedforward and recurrent
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M Jabri, B Flower - Neural Computation, 1991 - MIT Press
... is shown in Figure 1. For fully parallel **analog** implementations of ... **Bi-Directional** ...
flow for backpropagation training when fully **bidirectional** information paths ...
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R Douglas, M Mahowald, C Mead - Annual review of neuroscience, 1995 - Annual Reviews
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of factors, such as ease of implementation and the goal of the experiment. ...
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PP Fasang - ... Integrated Circuits Conference, 1989., Proceedings of ..., 1989 - ieeexplore.ieee.org
... FIGURE 5. **BIDIRECTIONAL** BOUNDARY-SCAN CELL ShiftDR ... 3. INPUT BOUNDARY-SCAN CELL 4.
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methods for **analog** and **digital** circuits is described. Fig. ...
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systems design viewpoints, combining both **analogue** and **digital** circuits on ...
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... FIGURE 5. **BIDIRECTIONAL** BOUNDARY-SCAN CELL ShiftDR ... in parallel, the **digital** patterns
used in the logic **simulation** of the ... FIGURE 6. **ANALOG** - **DIGITAL** ASIC WITH ...
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digital components ... Traditional **analog simulation** methods such as those used in ...
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Ellenberger, D.J.; Ng, Y.W.;
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Zhang Xiaopin; Ye Peida;
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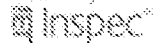
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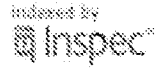
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